



S2AA THRU S2MA

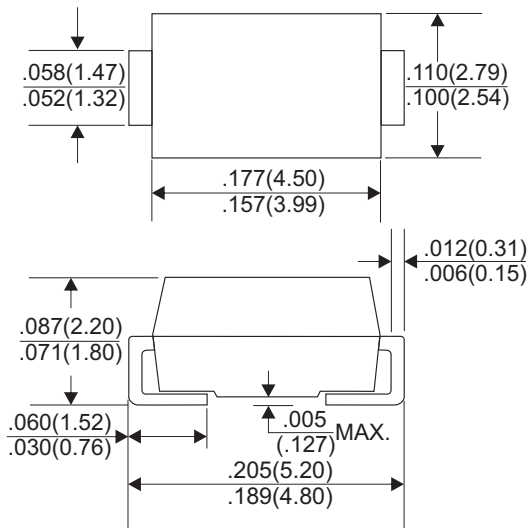
SURFACE MOUNT GENERAL PURPOSE SILICON RECTIFIERS

Reverse Voltage - 50 to 1000 Volts Forward Current - 2.0 Ampere

FEATURES

- * Glass passivate device
- * Ideal for surface mounted applications
- * Low reverse leakage
- * Metallurgically bonded construction
- * High temperature soldering guaranteed:
250°C/10 seconds 0.375"(9.5mm)lead length,
5 lbs.(2.3kg) tension

DO-214AC(SMA)



Dimensions in inches and (millimeters)

MECHANICAL DATA

- * Case: JEDEC SMA molded plastic
- * Terminals: Plated axial leads, solderable per MIL-STD-750, Method 2026
- * Polarity: Color band denotes cathode end
- * Mounting Position: Any

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwise specified.
Single phase half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

TYPE NUMBER	S2AA	S2BA	S2DA	S2GA	S2JA	S2KA	S2MA	UNITS
Maximum Recurrent Peak Reverse Voltage	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Length at Ta=75°C	2.0							A
Peak Forward Surge Current, 8.3 ms single half sine-wave superimposed on rated load (JEDEC method)	60							A
Maximum Instantaneous Forward Voltage at 2.0A	1.1							V
Maximum DC Reverse Current Ta=25°C	5.0							μA
at Rated DC Blocking Voltage Ta=100°C	50							μA
Typical Junction Capacitance (Note 1)	30							pF
Typical Thermal Resistance RθJA (Note 2)	20							°C/W
Operating and Storage Temperature Range Tj, Tstg	-65 — +175							°C
Marking Code	S2A	S2B	S2D	S2G	S2J	S2K	S2M	

NOTES:

1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
2. Thermal Resistance from Junction to Ambient.

RATING AND CHARACTERISTIC CURVES (S2AA THRU S2MA)

FIG.1-TYPICAL FORWARD CHARACTERISTICS

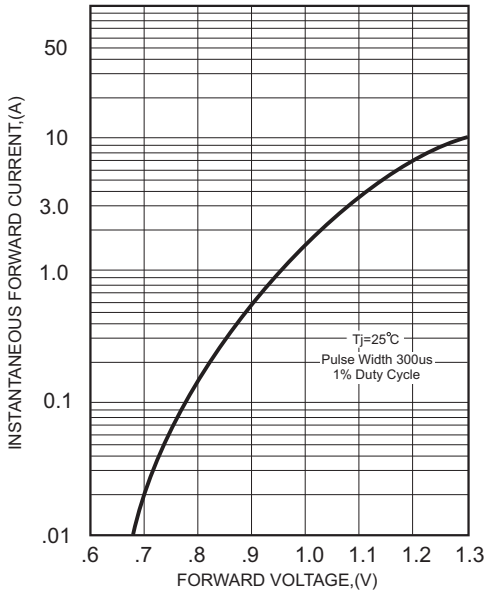


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

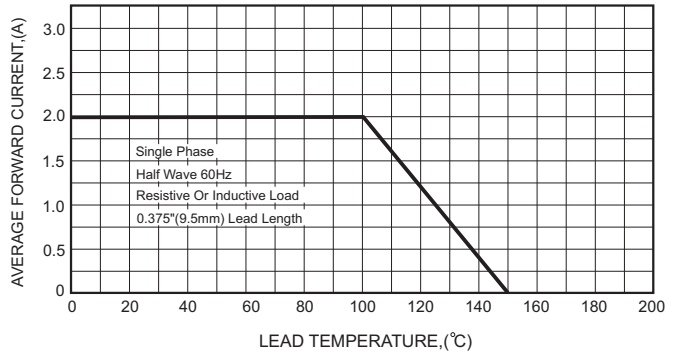


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

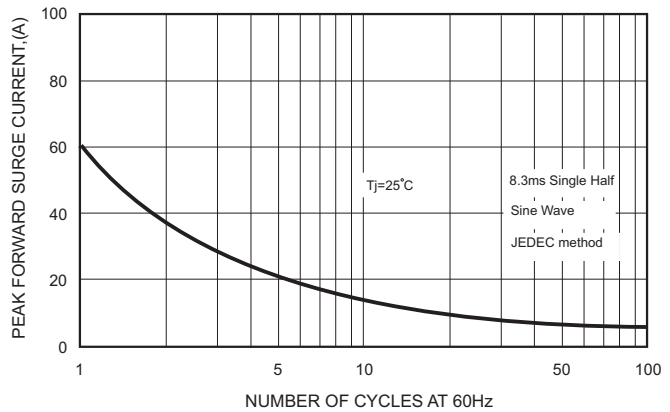


FIG.3 - TYPICAL REVERSE CHARACTERISTICS

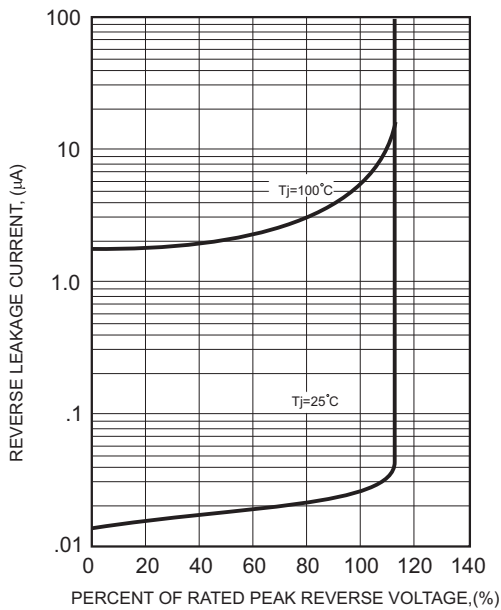


FIG.5-TYPICAL JUNCTION CAPACITANCE

